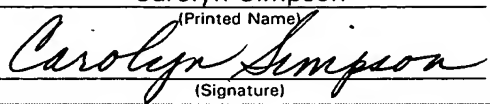


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Xiang et al.
Title: FULLY DEPLETED STRAINED
SILICON ON INSULATOR
TRANSISTOR AND METHOD
OF MAKING THE SAME
Appl. No.:
Filing Date:
Examiner:
Art Unit:

CERTIFICATE OF EXPRESS MAILING	
I hereby certify that this correspondence is being deposited with the United States Postal Service's "Express Mail Post Office To Addressee" service under 37 C.F.R. § 1.10 on the date indicated below and is addressed to: Commissioner for Patents, PO Box 1450, Alexandria, Virginia 22313-1450.	
EL 979078689	2/5/04
(Express Mail Label Number)	(Date of Deposit)
Carolyn Simpson	
(Printed Name)	
	
(Signature)	

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §1.56

Mail Stop PATENT APPLICATION
Commissioner for Patents
PO Box 1450
Alexandria, Virginia 22313-1450

Sir:

Submitted herewith on Form PTO-1449 is a listing of documents known to Applicants in order to comply with Applicants' duty of disclosure pursuant to 37 CFR §1.56. A copy of each listed document is being submitted to comply with the provisions of 37 CFR §1.97 and §1.98.

The submission of any document herewith, which is not a statutory bar, is not intended as an admission that such document constitutes prior art against the claims of the present application or that such document is considered material to patentability as defined in 37 CFR §1.56(b). Applicants do not waive any rights to take any action which would be appropriate to antedate or otherwise remove as a competent reference any document which is determined to be a *prima facie* art reference against the claims of the present application.

TIMING OF THE DISCLOSURE

The listed documents are being submitted in compliance with 37 CFR §1.97(b), within three (3) months of the filing date of the application.

RELEVANCE OF EACH DOCUMENT

An English translation of the foreign-language documents is not readily available. However, the absence of such translation does not relieve the PTO from its duty to consider the submitted foreign language documents (37 CFR §1.98 and MPEP §609).

Applicants respectfully request that any listed document be considered by the Examiner and be made of record in the present application and that an initialed copy of Form PTO-1449 be returned in accordance with MPEP §609. The relevance of the non-English documents is as follows:

JP 61-180466 discloses a laminated-type semiconductor device including a NAND circuit and a NOR circuit.

JP 3-248433 discloses a transistor having polycrystalline film 5.

JP 3-288471 discloses a MOS element 3'.

JP 4-123439 discloses the use of a dummy gate 3.

JP 4-240762 discloses a device on an SOI substrate 1.

JP 5-160396 discloses a MOS FET between regions 2.

JP 5-206454 discloses a polysilicon film 6 and solid phase diffusion.

JP 8-017845 discloses an SIPOS film 10 and a polysilicon film 4.

JP 11-102907 discloses an amorphous silicon film 2 that is made into a polysilicon film.

The Commissioner is hereby authorized to charge any additional fees which may be required regarding this application under 37 CFR §§ 1.16-1.17, or credit any overpayment, to Deposit Account No. 06-1447. Should no proper payment be enclosed herewith, as by a check being in the wrong amount, unsigned, post-dated, otherwise improper or informal or even entirely missing, the Commissioner is authorized to charge the unpaid amount to Deposit Account No. 06-1447.

Respectfully submitted,

Date

2-5-04

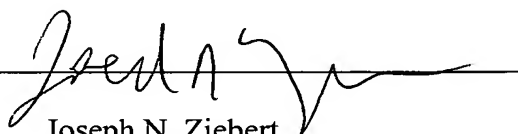
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By



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Attorney for Applicant
Registration No. 35,421

Form PTO-1449 (MODIFIED)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 039153-0649		SERIAL NO. To be determined		
INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>				APPLICANT Xiang et al.				
				FILING DATE To be determined		GROUP ART UNIT To be determined		
U.S. PATENT DOCUMENTS								
EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE IF APPROPRIATE	
	A1	4,683,645	08/04/87	Naguib et al.	437	41		
	A2	4,745,082	05/17/88	Kwok	437	39		
	A3	4,784,718	11/15/88	Mitani et al.	156	643		
	A4	4,789,644	12/06/88	Meda	437	41		
	A5	4,835,112	05/30/89	Pfiester et al.	437	24		
	A6	4,954,867	09/04/90	Hosaka	357	52		
	A7	4,996,574	02/26/91	Shirasaki	357	23.7		
FOREIGN PATENT DOCUMENTS								
	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION	
							YES	NO
	A95	61-180466	8/13/86	Japan				X
	A96	3-248433	11/06/91	Japan				X
	A97	3-288471	12/18/91	Japan				X
	A98	4-123439	4/23/92	Japan				X
OTHER DOCUMENTS <i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>								
	A104	Huang et al., Sub 50 nm FinFET: PMOS, , 1999 IEEE, 4 pages.						
	A105	Yu et al., Ultra-Thin-Body Silicon-On-Insulator MOSFET's For Terabit-Scale Integration; date unknown, pp. 623-626.						
	A106	Chatterjee et al., Sub-100nm Gate Length Metal Gate NMOS Transistors Fabricated by a Replacement Gate Process; 1997 IEEE, 5 pages.						
	A107	Wolf et al., Silicon Processing for the VLSI Era Vol. 1: Process Technology, 1986, 15 pages.						
	A108	Wolf, Silicon Processing for the VLSI Era Vol. 2: Process Integration, 9 pages.						
EXAMINER				DATE CONSIDERED				
* EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include any copy of this form with next communication to applicant.								

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U.S. PATENT DOCUMENTS							
EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE IF APPROPRIATE
	A8	4,998,150	03/05/91	Rodder et al.	357	23.1	
	A9	5,102,816	04/07/92	Manukonda et al.	437	44	
	A10	5,108,954	04/28/92	Sandhu et al.	437	200	
	A11	5,120,666	06/09/92	Gotou	437	40	
	A12	5,168,072	12/01/92	Moslehi	437	41	
	A13	5,200,352	04/06/93	Pfiester	437	44	
	A14	5,202,754	04/13/93	Bertin et al.	257	684	
	A15	5,258,637	11/02/93	Sandhu et al.	257	384	
	A16	5,264,382	11/23/93	Watanabe	437	44	
	A17	5,320,974	06/14/94	Hori et al.	437	44	
	A18	5,321,286	06/14/94	Koyama et al.	257	315	
	A19	5,346,834	09/13/94	Hisamoto et al.	437	41	
	A20	5,374,575	12/20/94	Kim et al.	437	44	
	A21	5,391,510	02/21/95	Hsu et al.	437	44	
	A22	5,393,685	02/28/95	Yoo et al.	437	44	
	A23	5,420,048	05/30/95	Kondo	437	21	
	A24	5,429,956	07/04/95	Shell et al.	437	29	
	A25	5,451,798	09/19/95	Tsuda et al.	257	139	
	A26	5,481,133	01/02/96	Hsu	257	621	
	A27	5,482,877	01/09/96	Rhee	437	40 RG	
	A28	5,491,099	02/13/96	Hsu	437	35	
	A29	5,497,019	03/05/96	Mayer et al.	257	347	
	A30	5,516,707	05/14/96	Loh et al.	437	24	
	A31	5,581,101	12/03/96	Ning et al.	257	347	
	A32	5,593,907	01/14/97	Anjum et al.	437	35	
	A33	5,595,919	01/21/97	Pan	437	29	
	A34	5,607,884	03/04/97	Byun	437	41	
	A35	5,612,546	03/18/97	Choi et al.	257	66	
	A36	5,612,552	03/18/97	Owens	257	202	

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U.S. PATENT DOCUMENTS							
EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE IF APPROPRIATE
	A37	5,654,570	08/05/97	Agnello	257	338	
	A38	5,675,159	10/07/97	Oku et al.	257	284	
	A39	5,675,185	10/07/97	Chen et al.	257	774	
	A40	5,689,136	11/18/97	Usami et al.	257	679	
	A41	5,716,861	02/10/98	Moslehi	437	40	
	A42	5,733,792	03/31/98	Masuoka	437	35	
	A43	5,736,446	04/07/98	Wu	438	305	
	A44	5,753,542	05/19/98	Yamazaki et al.	438	162	
	A45	5,789,792	08/04/98	Tsutsumi	257	506	
	A46	5,793,090	08/11/98	Gardner et al.	257	408	
	A47	5,811,323	09/22/98	Miyasaka et al.	438	151	
	A48	5,825,066	10/20/98	Buynoski	257	345	
	A49	5,851,869	12/22/98	Urayama	438	238	
	A50	5,856,225	01/05/99	Lee et al.	438	291	
	A51	5,858,843	01/12/99	Doyle et al.	438	299	
	A52	5,908,307	06/01/99	Talwar et al.	438	199	
	A53	5,915,182	06/22/99	Wu	438	299	
	A54	5,915,196	06/22/99	Mineji	438	526	
	A55	5,936,280	08/10/99	Liu	257	347	
	A56	5,953,616	09/14/99	Ahn	438	305	
	A57	5,960,270	09/28/99	Misra et al.	438	197	
	A58	5,985,726	11/16/99	Yu et al.	438	301	
	A59	6,008,111	12/28/99	Fushida et al.	438	584	
	A60	6,017,808	01/25/00	Wang et al.	438	528	
	A61	6,025,254	02/15/00	Doyle et al.	438	592	
	A62	6,031,269	02/29/00	Liu	257	347	
	A63	6,030,863	02/29/00	Chang et al.	438	229	
	A64	6,033,958	03/07/00	Chou et al.	438	275	
	A65	6,037,204	03/14/00	Chang et al.	438	231	

Form PTO-1449 (MODIFIED)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 039153-0649		SERIAL NO. To be determined	
INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>				APPLICANT Xiang et al.			
				FILING DATE To be determined		GROUP ART UNIT To be determined	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE IF APPROPRIATE
	A66	6,051,473	04/18/00	Ishida et al.	438	300	
	A67	6,057,200	05/02/00	Prall et al.	438	300	
	A68	6,083,798	07/04/00	Lin	438	297	
	A69	6,072,222	06/06/00	Nistler	257	383	
	A70	6,080,645	06/27/00	Pan	438	585	
	A71	6,087,235	07/11/00	Yu	438	300	
	A72	6,090,691	07/18/00	Ang et al.	438	564	
	A73	6,096,614	08/01/00	Wu	438	303	
	A74	6,103,609	08/15/00	Lee et al.	438	592	
	A75	6,110,783	08/29/00	Burr	438	286	
	A76	6,110,787	08/29/00	Chan et al.	438	300	
	A77	6,124,176	09/26/00	Togo	438	305	
	A78	6,137,149	10/24/00	Kodama	257	408	
	A79	6,150,221	11/21/00	Aoyama	438	300	
	A80	6,156,613	12/05/00	Wu	438	300	
	A81	6,171,910	01/09/01	Hobbs et al.	438	275	
	A82	6,184,097	02/06/01	Yu	438	299	
	A83	6,187,642	02/13/01	Yu et al.	438	300	
	A84	6,190,977	02/20/01	Wu	438	300	
	A85	6,200,867	03/13/01	Chen	438	300	
	A86	6,211,026	04/03/01	Ahmad et al.	438	300	
	A87	6,216,357	04/17/01	Victor et al.	33	810	
	A88	6,228,722	05/08/01	Lu	438	279	
	A89	6,248,637	06/19/01	Yu	438	300	
	A90	6,300,203	10/09/01	Buynoski et al.	438	287	
	A91	6,300,201	10/09/01	Shao et al.	438	281	
	A92	6,316,357	11/13/01	Lin et al.	438	662	
	A93	6,320,228	11/20/01	Yu	257	350	
	A94	6,380,043	04/30/02	Yu	438	305	

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